

SHARP SERVICE MANUAL

CODE : 00ZCE571BSM-E

2 MB RAM BOARD

MODEL **CE-571B**

FOR PC-5700 SERIES

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PARTS LIST

This document has been published to be used for after sales service only.
The contents are subject to change without notice.

SHARP CORPORATION

1. OVER VIEW

The CE-571B is a RAM card option of the PC-5700 SERIES to expand its main RAM capacity (2 MB).

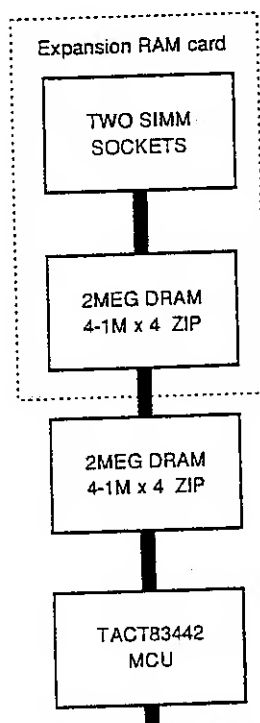
On the CE-571B are mounted four chips of 1MBit $\times$ 4 DRAM and 2 RAM SOCKETS.

CE-571B is expand max. 4MB (USED BY 1MB RAM MODULE) or 10MB (USED BY 4MB RAM MODULE).

1 MB RAM module TOSHIBA THM81000AS-80

4 MB RAM module TOSHIBA THM84000S-80

The RAM card option can be connected with the main RAM via the 60-pin connector (P11). The RAM card is controlled by the TACT83442 (MCU) outputs MA 0 $\sim$ 10, $\overline{\text{CAS}} 2 \sim \overline{\text{CAS}} 7$, $\overline{\text{RAS}} 1 \sim \overline{\text{RAS}} 3$, and data bus.



2. CIRCUIT DIAGRAM

NOTES : UNLESS OTHERWISE SPECIFIED :

1. ALL IC DEVICE TYPES ARE PREFIXED WITH SN74.
2. THE FOLLOWING PREFIX'S ARE ALWAYS USED:
T IS EQUAL TO "LS"
AT IS EQUAL TO "ALS"
3. THE FOLLOWING PREFIX'S ARE USED ONLY WHEN INSUFFICIENT CHARACTERS ARE AVAILABLE:
A IS EQUAL TO "ACT"
B IS EQUAL TO "BCT"
V IS EQUAL TO "VS"
H IS EQUAL TO "HT" OR "ALS"
4. IC PACKAGE TYPE IS INDICATED BY THE FOLLOWING SUFFIX'S :
DUAL-IN-LINE, PLASTIC = "N" OR BLANK
DUAL-IN-LINE, CERAMIC (HIDE) = "N"
DUAL-IN-LINE, CERAMIC (HIDE) = "J"
CHIP CARRIER, PLASTIC = "F"
CHIP CARRIER IN A S.H. SCKT = "FF"
CHIP CARRIER IN A PGA SCKT = "FE"
CHIP CARRIER, CERAMIC (RECT) = "FH"
CHIP CARRIER, CERAMIC (SQUARE) = "FU"
FLAT PACKAGE, CERAMIC (HIDE) = "W"
GRID ARRAY, PLASTIC (LIF SCKT) = "XL"
GRID ARRAY, PLASTIC (ZIF SCKT) = "XZ"
GRID ARRAY, CERAMIC = "V"
GRID ARRAY, CERAMIC (LIF SCKT) = "VZ"
GRID ARRAY, CERAMIC (ZIF SCKT) = "VZ"
SINGLE-IN-LINE = "E.L.M.G"
"SOIC", PLASTIC (HIDE) = "D"
"SOJ", PLASTIC, J LEADS = "R"

5. VCC IS APPLIED TO PIN 8 OF ALL 8-PIN IC'S, PIN 14 OF ALL 14-PIN IC'S, PIN 15 OF ALL 16-PIN IC'S, PIN 20 OF ALL 28-PIN IC'S, ETC.

6. GROUND IS APPLIED TO PIN 4 OF ALL 8-PIN IC'S, PIN 7 OF ALL 14-PIN IC'S, PIN 8 OF ALL 16-PIN IC'S, PIN 10 OF ALL 20-PIN IC'S, ETC.

7. DEVICE TYPE, PIN NUMBERS, AND REFERENCE DESIGNATOR (LOCATION) OF GATES ARE SHOWN AS FOLLOWS :



00 AND 04 = DEVICE TYPES
1, 2, AND 3 = PIN NUMBERS
U01 AND U02 = REF. DESIGNATOR (LOCATION)

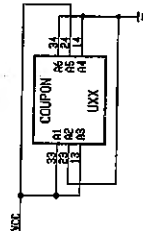
8. RESISTANCE VALUES ARE IN OHMS.

9. RESISTORS ARE 1/8 WATT, 5%.

10. CAPACITANCE VALUES ARE IN MICROFARADS.

11. CAPACITORS ARE 50V, 5%.

12. THIS COUPON WILL BE USED ON ALL COMMERCIAL MULTILAYER BOARDS.

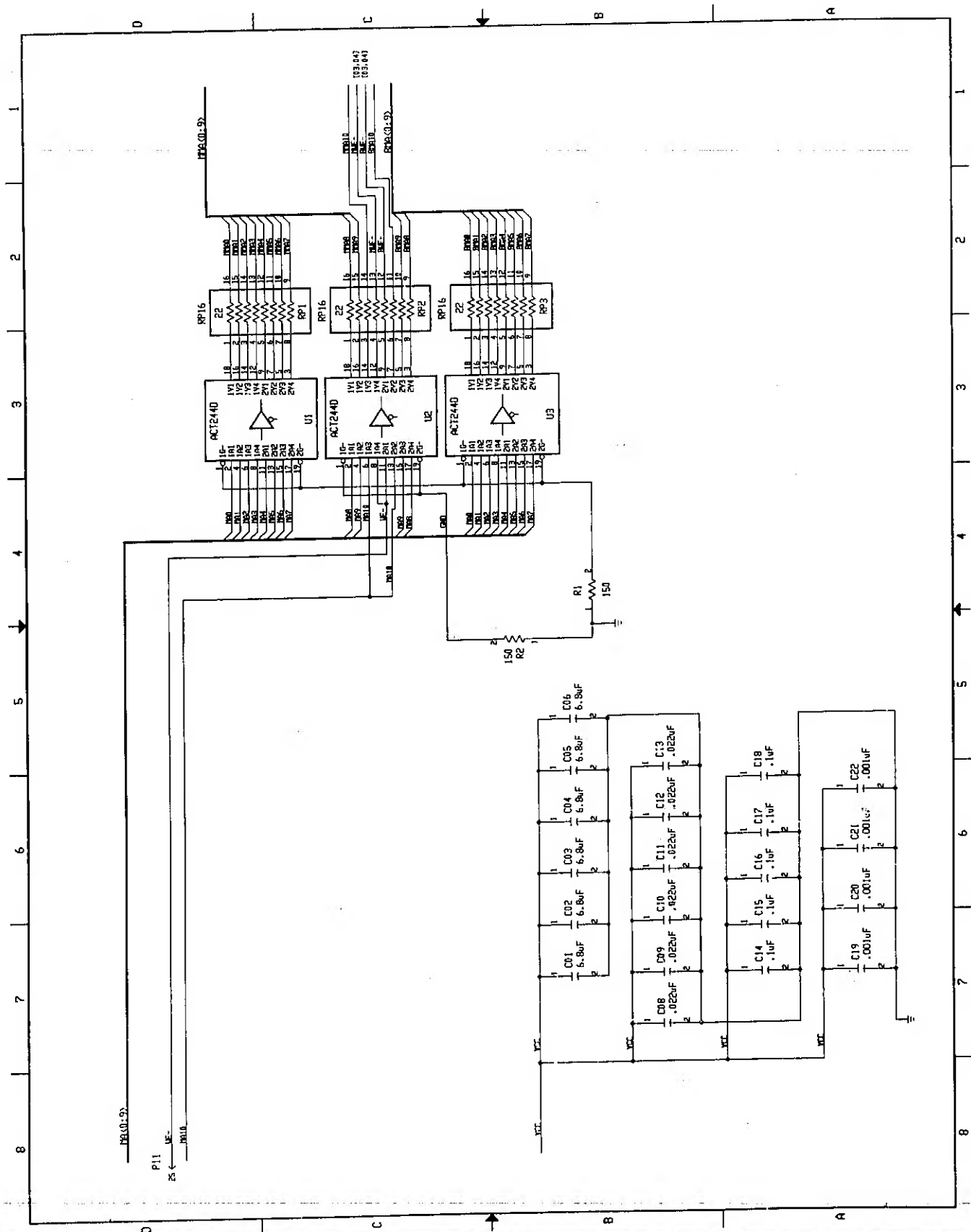


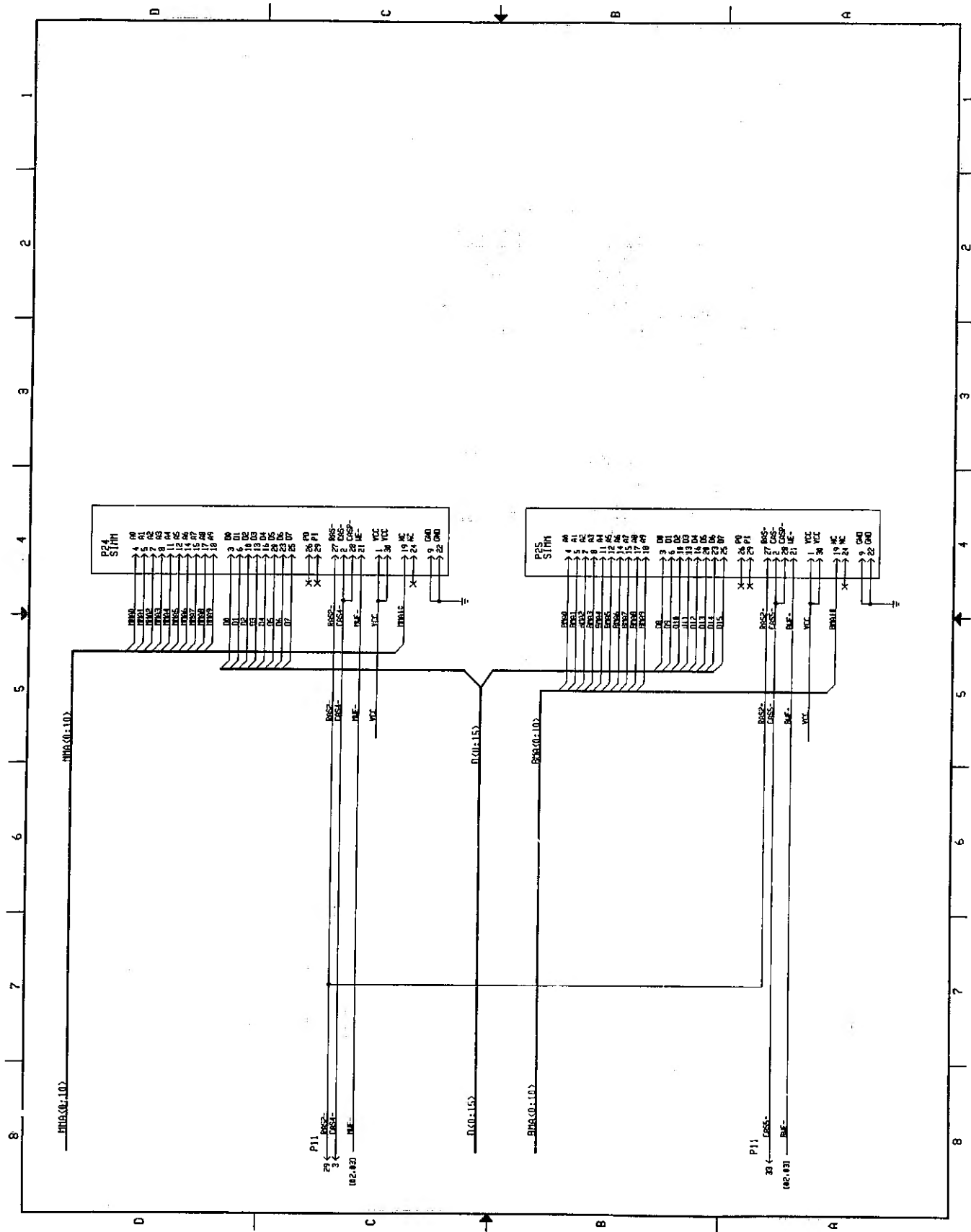
FORMAL RELEASE

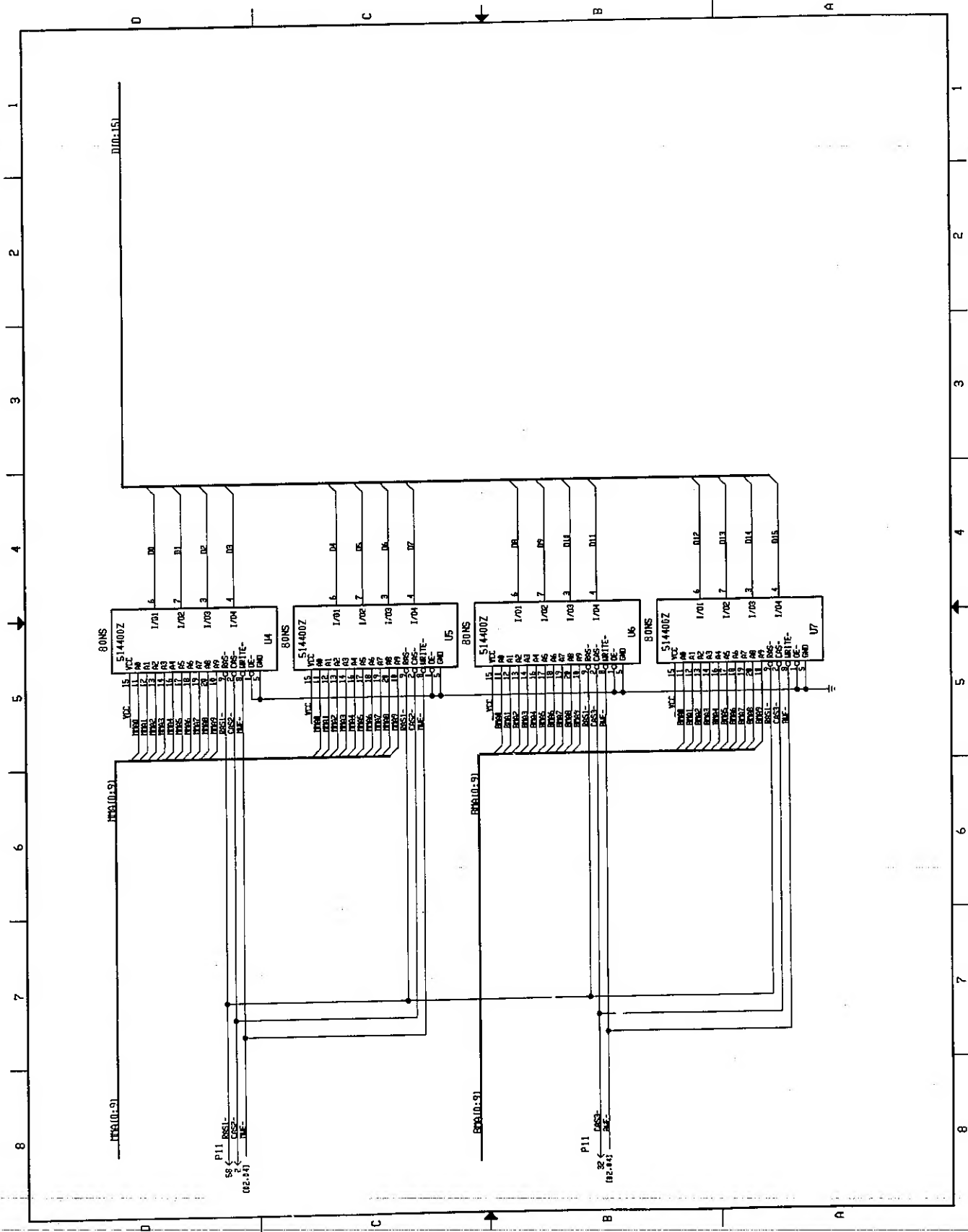
P11 EX-RAM

VCC	1	21	VCC
Q000	2	22	Q000
Q001	3	23	Q001
Q002	4	24	Q002
Q003	5	25	Q003
Q004	6	26	Q004
Q005	7	27	Q005
Q006	8	28	Q006
Q007	9	29	Q007
Q008	10	30	Q008
Q009	11	31	Q009
Q010	12	32	Q010
Q011	13	33	Q011
Q012	14	34	Q012
Q013	15	35	Q013
Q014	16	36	Q014
Q015	17	37	Q015
Q016	18	38	Q016
Q017	19	39	Q017
Q018	20	40	Q018
Q019	21	41	Q019
Q020	22	42	Q020
Q021	23	43	Q021
Q022	24	44	Q022
Q023	25	45	Q023
Q024	26	46	Q024
Q025	27	47	Q025
Q026	28	48	Q026
Q027	29	49	Q027
Q028	30	50	Q028
Q029	31	51	Q029
Q030	32	52	Q030
Q031	33	53	Q031
Q032	34	54	Q032
Q033	35	55	Q033
Q034	36	56	Q034
Q035	37	57	Q035
Q036	38	58	Q036
Q037	39	59	Q037
Q038	40	60	Q038

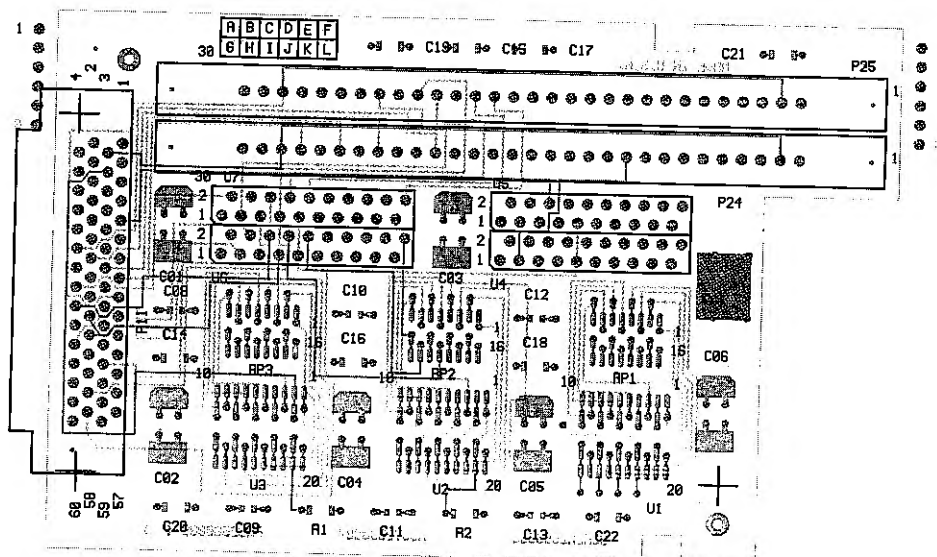
COMPUTER GENERATED DRAWING
DO NOT REVISE MANUALLY







3. P. W. B. LAYOUT



PARTS LIST

CE-571B

NO	PARTS CODE	PRICE RANK	NEW MARK	PART RANK	DESCRIPTION
1	OII2221301007	A H	N	C	Capacitor (6.8 μ F $\pm$ 20%) [C1~C6]
2	VCKYTQ1HB223M	A A	N	C	Capacitor (50wv 22000pF) [C8~C13]
3	VCKYTQ1EF104Z	A A		C	Capacitor (25wv 0.1 μ F) [C14~C18]
4	VCKYTQ1HB102K	A A		C	Capacitor (50wv 1000pF) [C19~C22]
5	VRS-TP2BD151J	A A		C	Resistor (1/8w 150 Ω $\pm$ 5%) [R1, R2]
6	OII2248301042	A N		C	Resistor (22 Ω 2%) [RP1~RP3]
7	OII2548291002	A R		B	IC (ACT244) [U1~U3]
8	QCNCM5079SC6J	A P		C	Connector (60pin) [P1]
9	VHI514400Z//8	B E		B	IC (51440Z) [U4~U7]
10	QSOCZ2025HCZZ	A R		C	IC socket (30pin) [P24, P25]

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